



PTO/SB/08a (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 Of 1

Complete if Known

Application Number	10/736,238
Filing Date	December 12, 2003
First Named Inventor	Hua-Jan Lo
Group Art Unit	
Examiner Name	
Attorney Docket Number	3304.2.105

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ^{2 (if known)}			
AJ	U1	US-6,025,792-B1	02/15/2000	Smith	

Examiner Signature		Date Considered	3/3/05
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Sheet 1 Of 1

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Group Art Unit	
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AT	01	Liu, Xiang-yuan, et al., 'The Study and Design of High-Speed LVDS I/O Interface Cells,' Computer Engineering & Science, Vol. 23, No. 4, 2001. page 52-57.	

Examiner Signature		Date Considered	3/31/05
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